

PAL16L8

DPALCAS 252128-02 REV 3

15-MAY-85

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/ARW A20 A19 /PRW /UDS /LDS /ROME /RE /RGAE GND /DAE /ROM01  
/C1 /RRW LCEN UCEN /CDR /CDW /NC1 VCC

ROM01 =ROME\*A20\*A19\*/PWR + ROME\*/A20\*/A19\*/PRW  
CDR =RE\*/PWR\*/C1 + RGAE\*/PRW\*/C1 + CDR\*LDS + CDR\*UDS  
CDW =RE\*PRW + RGAE\*PRW + CDW\*/C1 + /DAE\*/UDS\*/UCEN  
/UCEN =/DAE\*/RE\*/UCEN + /DAE\*/RE\*C1 +/DAE\*UDS\*C1 + /DAE\*/UDS\*/UCEN  
/LCEN =/DAE\*/RE\*/LCEN + /DAE\*/RE\*C1 +/DAE\*/LDS\*C1 + /DAE\*/LDS\*/LCEN  
RRW =RE\*PRW + DAE\*ARW\*C1 + RRW\*DAE

Description

In RRW equation, may not need RRW\*/C1 hold term, but doesn't hurt

*PRELIMINARY*

PAL16L8

DPALEN FOR RAMROM BOARD 252128-01 REV 2

15-MAY-85

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A23 A22 A21 /AS /DBR OVL /OVR XRDY /C3 GND / C1 /VPA  
/MYRAME /DAE /RGAE /RE /DTACK / BLS NC VCC

IF(/OVR) VPA =AS° A23°/A22° A21

MYRAME = AS°/DTACK°A23°A22°A21°/OVR°C1°/C3 +  
AS°/DTACK°/A23°/A22°/A21°/OVR°OVL°C1°/C3 +  
MYRAME°C1 +  
MYRAME°C3 +

RE = /DBR°AS/DTACK°/A23°/A22°/A21°/OVL° C1°/C3°/OVR +  
RE° C1 + RE° C3

IF(/OVR) DTACK =

AS°/A23°/A22° A21° XRDY +  
AS°/A23° A22° XRDY +  
AS° A23°/A22°/A21° XRDY +  
MYRAME°XRDY°C3 +  
RE°C3 +

RGAE°C3 + DTACK°AS°XRDY  
RGAE =/DBR°AS°/DTACK° A23° A22°/A21° C1°/C3°/OVR +  
RGAE°C1 + RGAE° C3

DAE =DBR° C1°/C3 +  
DAE° C1 + DAE° C3

BLS = AS°/DTACK°/A23°/A22°/A21°/OVL° C1°/C3°/OVR +  
AS°/DTACK° A23° A22°/A21 C1°/C3°/OVR +  
BLS°C1 +BLS°C3

#### Description

This is the RAMROM board version of the PALEN PAL of the main board. When it returns to the main board, rethink the MYRAME and DTACK stuff.

PRELIMINARY

PAL 16L8

DAUGCAS ram/rom CAS 252128-04 REV 1

16-APR-85

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/SPROM A18 A17 /PRW /UDS /LDS /RE /RES /ROME GND /C1 /BERR  
/WPRO /RRW /LCEN /UCEN /CDR /CDW /ROMO1 VCC

ROMO1 =ROME°/A18°/WPRO°/SROM°/PRW  
CDR =CDR°LDS + CDR°UDS + RE°/PRW°/C1°A18 + RE°/PRW°/C1°/A18°WPRO  
+ RE°/PRW°/C1/A18°SROM  
CDW = RE°PRW + CDW°/C1  
UCEN =UCEN°/C1 + RE°UDS°A18 RE°UDS°/A18°WPRO +  
RE°UDS°/A18°SROM  
LCEN =LCEN°/C1 + RE°LDS°A18 + RE°LDS°/A18°WPRO +  
RE°LDS°/A18°SROM  
RRW =RE°PRW°A18/WPRO°/SROM  
BERR =WPRO°PRW°RE  
WPRO =WPRO°/RES + PRW°RE/A18

#### Description

This is the CAS PAL for the RAMROM board

PRELIMINARY

PAL16L8

DAUGEN PAL FOR RAMROM BOARD 252128-03 REV 2

15-MAY 85

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A23 A22 A21 /AS /DBR OVL /OVR XRDY /C3 GND /C1 /CNT  
/RE DAE /NC1 A19 /DTACK A20 /ROME VCC

/CNT =DAE°C1°C3

RE =AS°/DTACK°A23°A22°A21°A20°A19°/OVR°C1°/C3 +  
AS°/DTACK°/A23°/A22°/A21°/A20°/A19°/OVR°OVL°C1°/C3 +  
RE°C1 + RE°C3

/DAE =/C1°/C3 + RE +  
AS°/DTACK°A23°A22°A21°A20°A19°/OVR°OVL°C1°/C3 +  
AS°/DTACK°/A23°/A22°/A21°/A20°/A19°/OVR°OVR°C1°/C3

ROME =AS°A23°A22°A21°A20°A19°/OVR +  
AS°/A23°/A22°/A21°/A20°/A19°/OVR°OVL

#### Description

This is the daughter RAM/ROM DAUGEN PAL

PRELIMINARY